

FIG. A

79-04

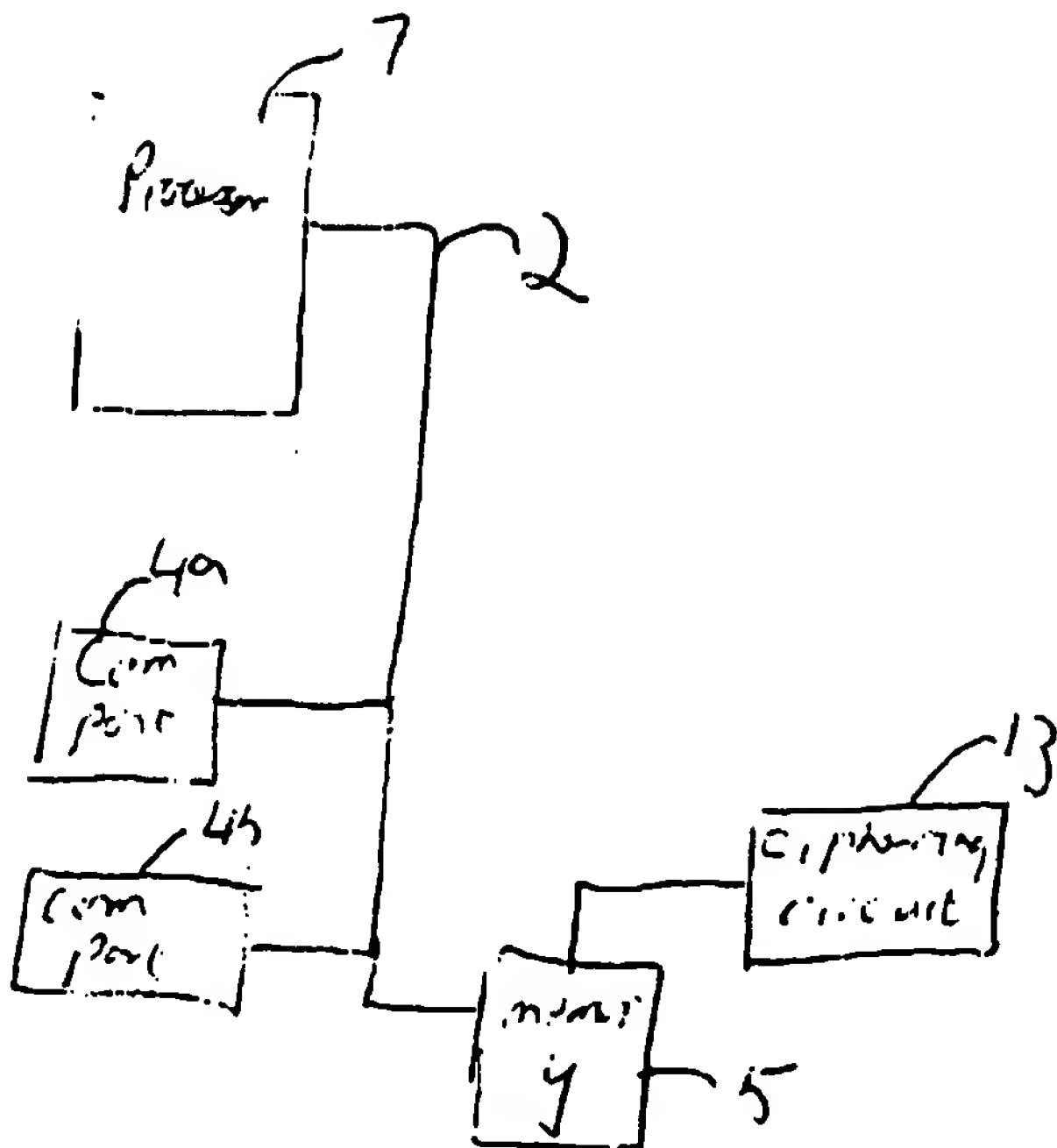


Fig. 2

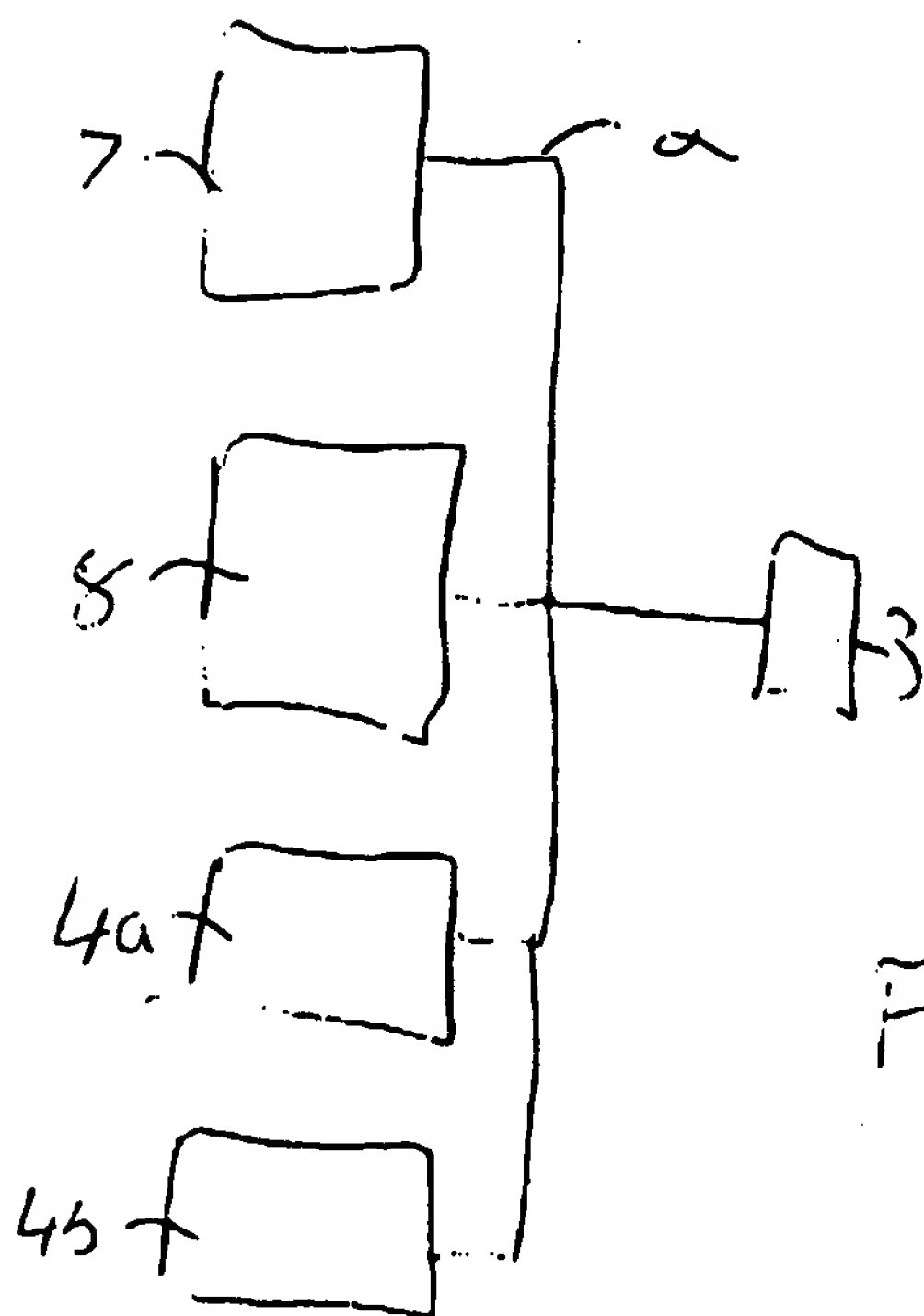


Fig. 1

FPGA
37-04

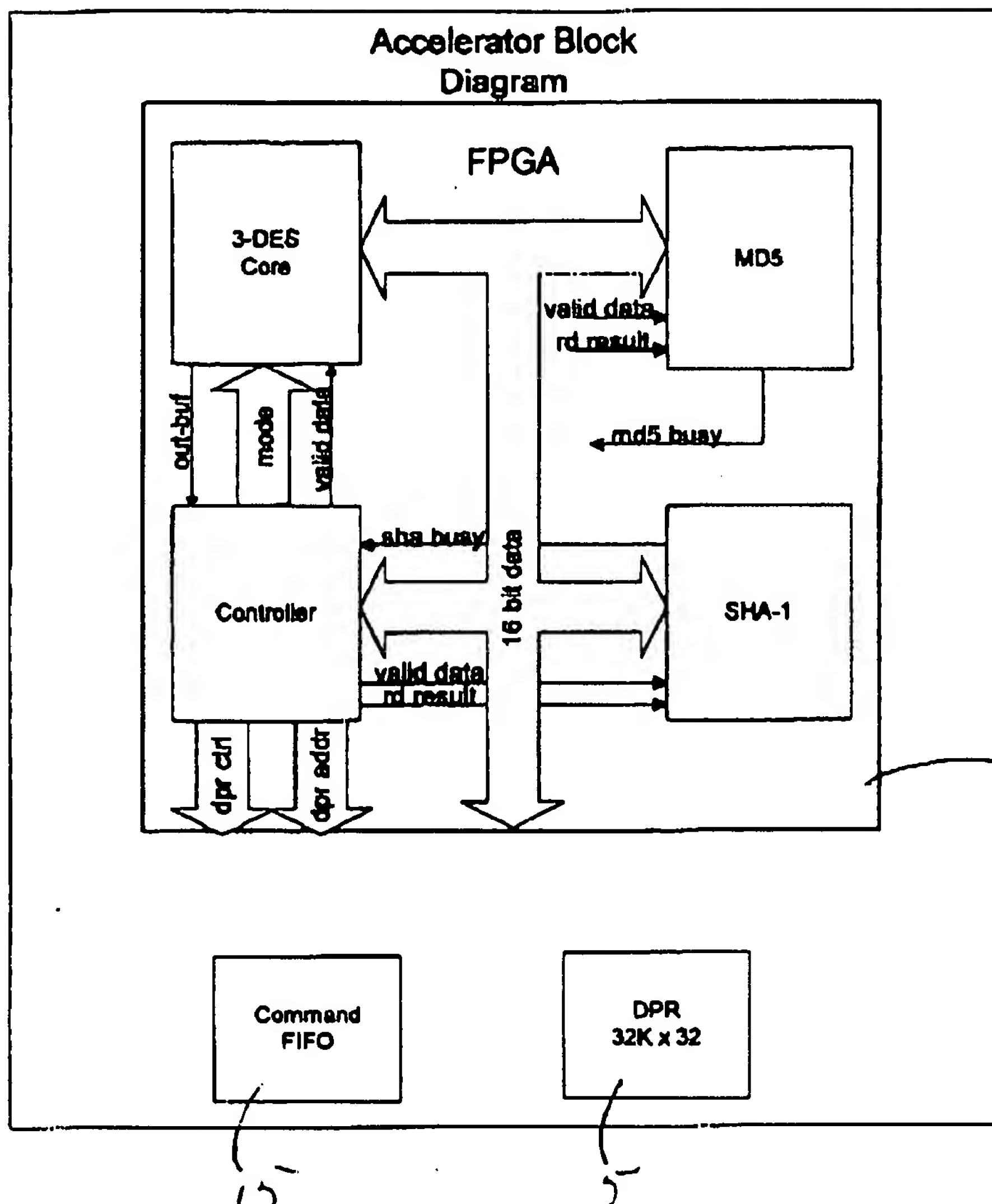


fig. 3